



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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DEC 1 2003

In re Application of:

Bendek et al

Group No.: 2661

TC 2600

Serial No.: 09/747,380

Examiner: Unknown

Filed: December 22, 2000

Docket No. AMCC4860

#6

For: **SYSTEM AND METHOD FOR PROGRAMMING THE BIT ERROR RATE
OF FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL
DIGITAL FRAME STRUCTURE**

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date November 20, 2003, in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA. 22313-1450

November 20, 2003
Date

Terrance A. Meador
Signature

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Mail Stop: Change of Correspondence Address
Commissioner for Patents
P. O. Box 1450
Alexandria, VA. 22313-1450

NOV 28 2003

Technology Center 2600

CHANGE OF CORRESPONDENCE ADDRESS

The correspondence address of the applicant's representative has changed.

Please now address all correspondence in this application to:

Terrance A. Meador
INCAPLAW
1050 Rosecrans Street, Suite K
San Diego, CA. 92106

The applicant's representative may also be contacted by telephone, facsimile, and email as follows:

619-222-2531 (Office)
619-222-2327 (FAX)
tmeador@incaplaw.com

Respectfully submitted,

Terrance A. Meador

TERRANCE A. MEADOR
Reg. No. 30, 298

Date: November 20, 2003